Err amdt (1)



18. (previously presented) A signal amplifier, characterized by comprising a pre-stage circuit for inputting a signal from an external device and a post-stage circuit for amplifying the signal fed from the pre-stage circuit and outputting the signal therefrom, wherein:

input impedance of the pre-stage circuit and output impedance of the post-stage circuit are set to match external impedance in a ~~one or more-digit~~ of at least several tens of Khz to several tens of Ghz frequency range; and

output impedance of the pre-stage circuit and input impedance of the post-stage circuit are set to match at impedance lower than the output impedance of the post-stage circuit; and

wherein an output section of the pre-stage circuit is connected via a capacitor to an input section of the post-stage circuit.

19. (previously presented) A signal amplifier, characterized by comprising a pre-stage circuit for inputting a signal from an external device and a post-stage circuit for amplifying the signal fed from the pre-stage circuit and outputting the signal therefrom, wherein:

input impedance of the pre-stage circuit and output impedance of the post-stage circuit are set to match external impedance in a ~~one or more-digit~~ of at least several tens of Khz to several tens of Ghz frequency range; and

Exr amdt (d)



output impedance of the pre-stage circuit and input impedance of the post-stage circuit are set to match at impedance lower than the output impedance of the post-stage circuit;

wherein the pre-stage circuit includes an impedance transforming circuit; and

wherein the impedance transforming circuit includes a differential circuit.

20. (previously presented) A signal amplifier, characterized by comprising a pre-stage circuit for inputting a signal from an external device and a post-stage circuit for amplifying the signal fed from the pre-stage circuit and outputting the signal therefrom, wherein:

input impedance of the pre-stage circuit and output impedance of the post-stage circuit are set to match external impedance in a ~~one or more digit~~ of at least several tens of KHz to several tens of GHz frequency range; and

output impedance of the pre-stage circuit and input impedance of the post-stage circuit are set to match at impedance lower than the output impedance of the post-stage circuit;

wherein the post-stage circuit includes a traveling-wave amplifier; and

wherein an output section of the pre-stage circuit is connected via a capacitor to an input section of the post-stage circuit.

Err amdt (3)



21. (previously presented) A signal amplifier, characterized by comprising a pre-stage circuit for inputting a signal from an external device and a post-stage circuit for amplifying the signal fed from the pre-stage circuit and outputting the signal therefrom, wherein:

input impedance of the pre-stage circuit and output impedance of the post-stage circuit are set to match external impedance in a ~~one-or-more-digit~~ of at least several tens of Khz to several tens of Ghz frequency range, and

output impedance of the pre-stage circuit and input impedance of the post-stage circuit are set to match at impedance lower than the output impedance of the post-stage circuit;

wherein the pre-stage circuit includes an impedance transforming circuit; and

wherein an output section of the pre-stage circuit is connected via a capacitor to an input section of the post-stage circuit.

22-23. (canceled)

24. (previously presented) A signal amplifier in accordance with claim 1, wherein an output section of the pre-stage circuit is connected via a capacitor to an input section of the post-stage circuit.